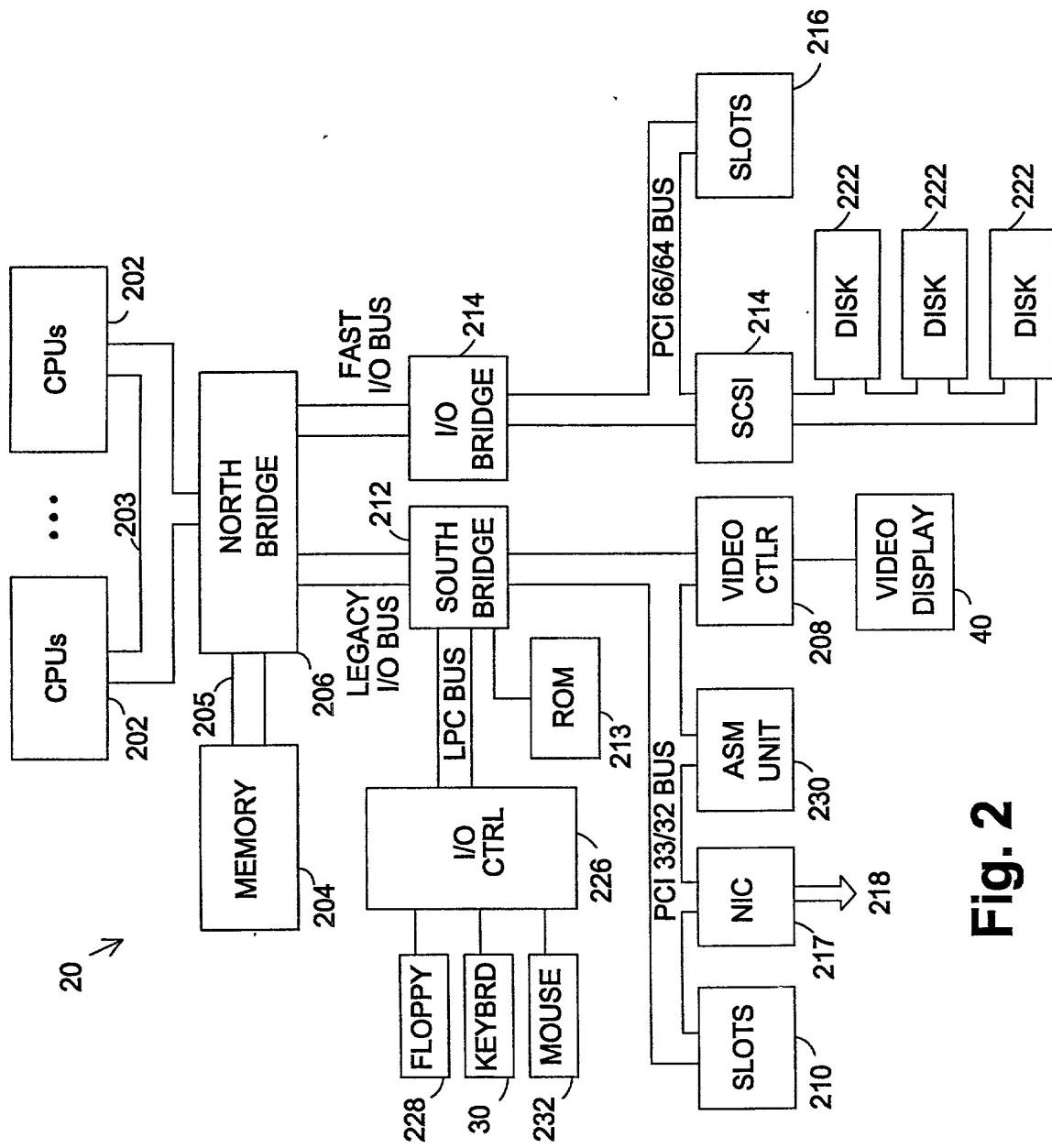


Fig. 1

**Fig. 2**

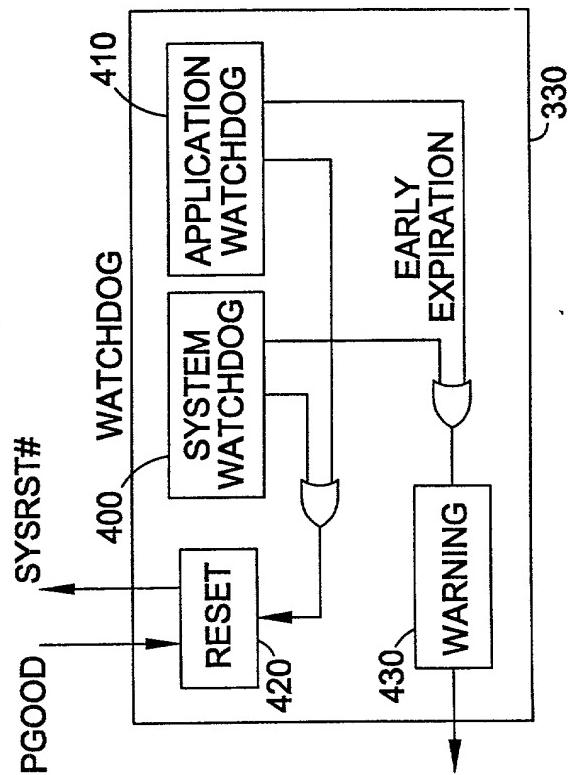


Fig. 4

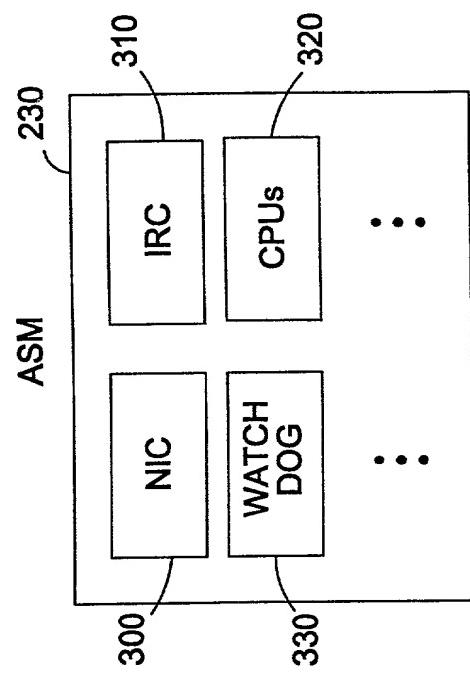
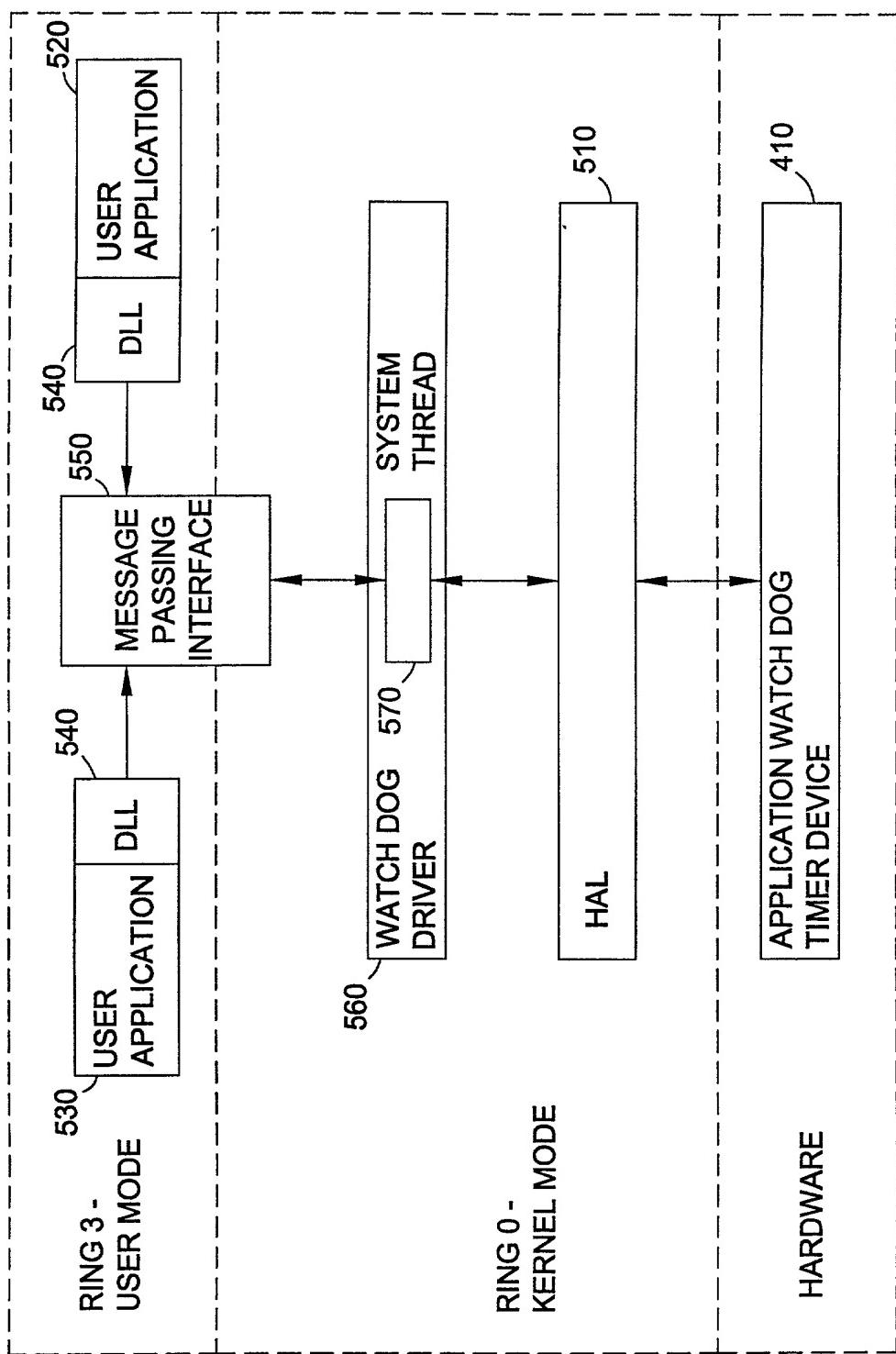


Fig. 3

**Fig. 5**

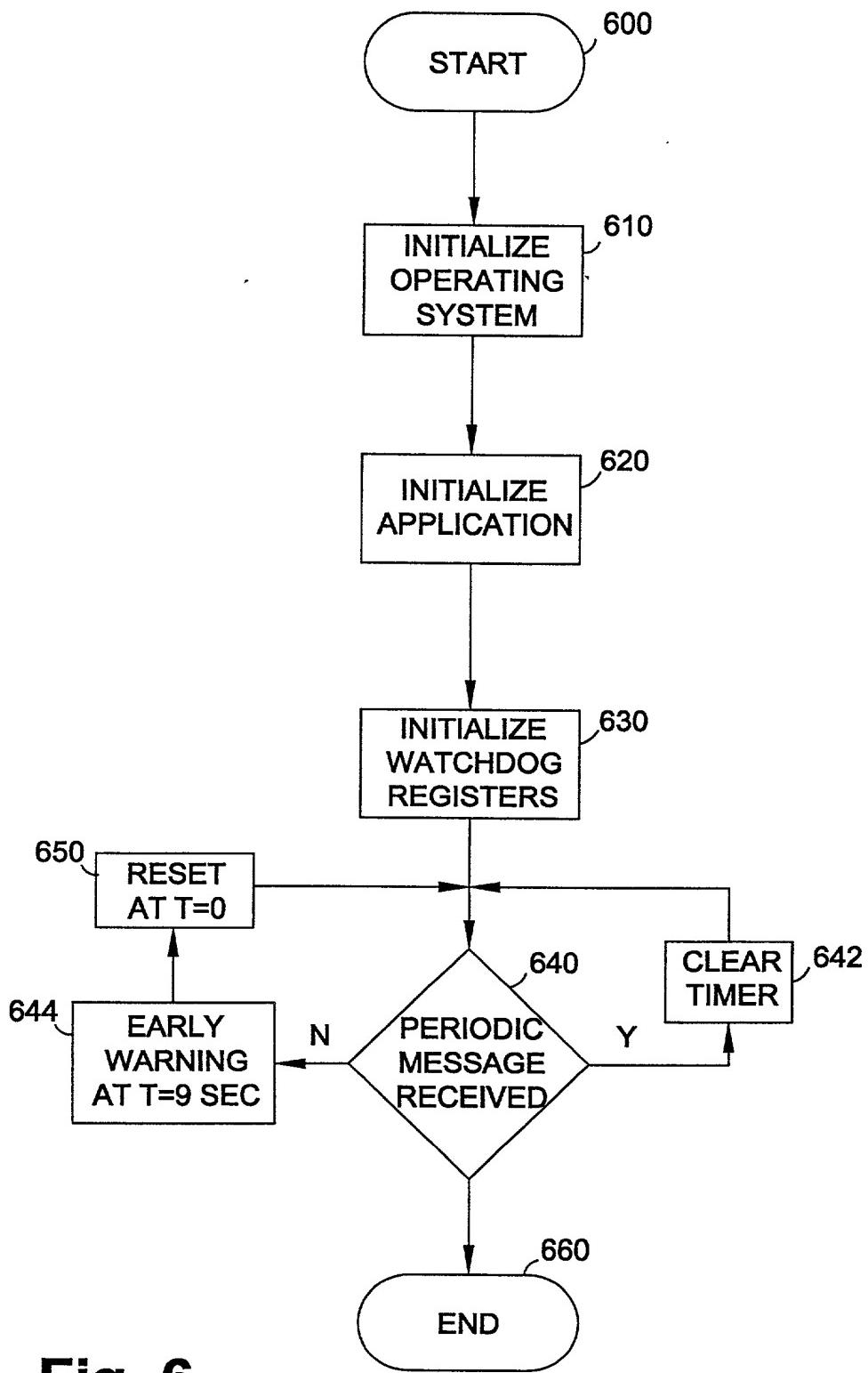


Fig. 6

APPLICATION WATCHDOG TIMER VALUE REGISTER			
LOC	BIT	READ/WRITE	RESET
15:0	COUNT	R/W	0

APPLICATION WATCHDOG TIMER CONTROL/STATUS REGISTER			
LOC	BIT	READ/WRITE	RESET
7	NMISTAT	R/W	0
6	SMISTAT	R/W	0
5:4	RESERVED	R	0
3	SMIEN	R/W	0
2	NMIEN	R/W	0
1	RELOAD	W	0
0	ENABLE	R/W	0

Fig. 7